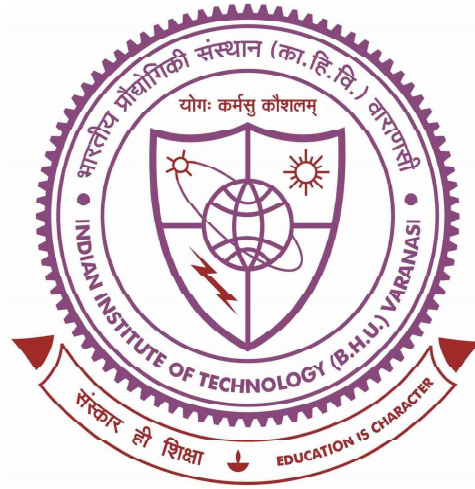


Algorithms and their VLSI Architecture to Enhance Image Quality for Real-time Applications



**Thesis submitted in partial fulfilment
for the Award of Degree**

DOCTOR OF PHILOSOPHY

by

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