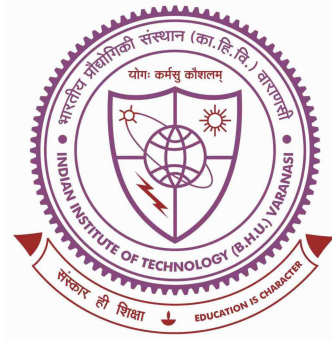


SVM Techniques for Common Mode Voltage Minimization in High Gain Ultra Sparse Matrix Converters



Thesis submitted in partial fulfillment
for the award of degree

Doctor of Philosophy

by

Mr. Vulavakayala Siva

DEPARTMENT OF ELECTRICAL ENGINEERING
Indian Institute of Technology
(Banaras Hindu University)
Varanasi