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# Appendix A

## List of Publications

### A.1 Patent

- **Vulavakayala siva**, S. K. Singh “An Ultra-Sparse Matrix Converter with Auxiliary Shoot-Through Switches.” (Published)

### A.2 Journal papers

- **Vulavakayala siva**, M. Raghuram, A. Singh, S. K. Singh and Y. P. Siwakoti, “Switching Strategy to Reduce Inductor Current Ripple and Common Mode Voltage in Quasi Z-Source Ultra Sparse Matrix Converter,” in *IEEE Journal of Emerging and Selected Topics in Industrial Electronics*, vol. 4, no. 4, pp. 1159-1169, Oct. 2023, doi: 10.1109/JESTIE.2023.3268589.
- **Vulavakayala siva**, M. Raghuram, A. Singh and S. K. Singh, “Analysis and Control of Impedance Network Integrated High Voltage Gain USMC for Wider Output Frequency,” in *IEEE Transactions on Industrial Electronics*, vol. 71, no. 9, pp. 10098-10107, Sept. 2024, doi: 10.1109/TIE.2023.3344828.
- **Vulavakayala siva**, M. Raghuram and S. K. Singh, “Stabilized High Voltage Gain With Reduced Common Mode Voltage Using SN-USMC for Remote Green EV Charging,” in *IEEE Journal of Emerging and Selected Topics in Industrial Electronics* vol. 5, no. 2, pp. 577-585, April 2024, doi: 10.1109/JESTIE.2024.3355886.
- **Vulavakayala siva**, and Santosh K. Singh. “Mitigation of common mode voltage

in ultra sparse matrixconverters using auxiliary shoot-through switches for wind energy systems”. *Int J Circ Theor Appl.* 2024;1-19. doi:10.1002/cta.3963.

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### A.3 Journal papers (not related to thesis)

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