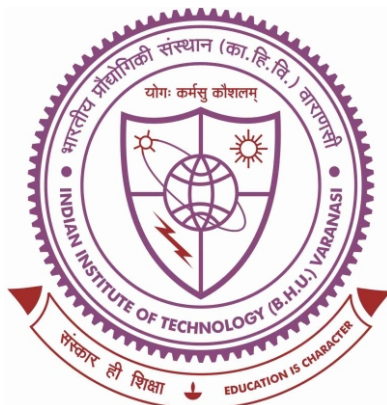


# DEVICE MODELING AND LOGIC CIRCUITS DESIGN FOR SPIN-BASED COMPUTING



Thesis submitted in the partial fulfillment for the  
Award of Degree

**Doctor of Philosophy**

By

*Jagadish*

DEPARTMENT OF ELECTRONICS ENGINEERING  
INDIAN INSTITUTE OF TECHNOLOGY  
(BANARAS HINDU UNIVERSITY)  
VARANASI - 221005  
INDIA

Roll No. 19091501

Year 2024